

Abstract of the disclosure

The present invention relates to a fringe field switching mode liquid crystal display, comprising: gate
5 lines and data lines aligned on a transparent insulating substrate to vertically cross each other; common electrode lines aligned horizontally to the gate lines; a pixel region defined in a space formed by the gate lines and data lines; a first transparent electrode formed in the pixel region,
10 and divided into at least two regions; and a second transparent electrode insulated from the first transparent electrode, and divided on the first transparent electrode as many as the first transparent electrode, a data voltage being applied to the second transparent electrode in a first
15 region and to the first transparent electrode in a second region, a sum of the voltages applied to the pixel region having a zero voltage.